# Interpretative Semantics for prialt-free Handel-C 

Andrew Butterfield

December 20, 2001

## Contents

1 Introduction ..... 2
2 Reduced Abstract Syntax ..... 3
2.1 Program (§9.3.17, p197) ..... 3
2.2 Declarations (§9.3.6, p189) ..... 4
2.3 Statements (§9.3.16, pp196-7) ..... 4
2.4 prialt Cases ( $\S 9.3 .16$, p197) ..... 4
2.5 Expressions (§9.3.15, pp194-5) ..... 5
2.6 Identifier (§9.3.1, p186) ..... 5
2.7 Abstract Syntax Summary ..... 6
3 Explicit Environment Abstract Syntax ..... 6
3.1 Trees ..... 7
3.1.1 Tree Type ..... 7
3.1.2 Tree Access Functions ..... 7
3.2 Tree Cursors ..... 7
3.2.1 Tree Cursor Type ..... 7
3.2.2 Manipulating Tree Cursors ..... 7
3.2.3 Tree Lookup ..... 8
3.3 Tree Environments ..... 8
3.3.1 Cursor-Tagged Identifiers ..... 8
3.3.2 Tree Environment Type ..... 8
3.3.3 Tree Environment Lookup/Update ..... 8
3.3.4 Tree Environment Key Lookup ..... 8
3.4 Variable Data and Channel States ..... 8
3.5 Explicit Environment Syntax ..... 9
3.5.1 Statements revisited ..... 9
3.5.2 prialt Cases revisited ..... 9
3.5.3 Expression revisited ..... 10
3.5.4 Abstract Syntax Tree revisited ..... 10
3.5.5 Program revisited ..... 10
3.6 Build the Explicit Environment Tree ..... 11
3.6.1 Converting Prg ..... 11
3.6.2 Converting InitDecl ..... 11
3.6.3 Converting Decl ..... 11
3.6.4 Converting Stmt ..... 12
3.6.5 Converting PCase ..... 13
3.7 Explicit Environment Syntax Summary ..... 14
4 Control Flow (Interpretative) Semantics ..... 15
4.1 Introduction ..... 15
4.2 Running the Program ..... 15
4.3 The Clock Cycle ..... 16
4.3.1 Evaluating the RHS ..... 16
4.3.2 'Incrementing' Execution Pointers ..... 17
4.4 Determining Next Execution Pointers ..... 18
4.5 Definitions of fstep and nxtep ..... 18
4.5.1 The SEQ Statement ..... 20
4.5.2 The IF Statement ..... 21
4.5.3 The Whl Statement ..... 23
4.5.4 The Par Statement ..... 24
4.5.5 The Brk Statement ..... 26
4.5.6 The Pri Statement ..... 27
4.6 Definition of resep ..... 28
4.6.1 Definition of resep, without prialts ..... 28
4.7 Full Definitions of fstep and nxtep ..... 28
4.7.1 Complete Definition of fstep ..... 29
4.7.2 Complete Definition of nxtep ..... 30
4.8 Auxilliary Functions ..... 30
5 Acknowledgments ..... 31

## 1 Introduction

Handel-C [Emb] is a language developed by the Hardware Compilation Group at Oxford's Computer Laboratory. It is a hybrid of C and CSP [Hoa90], designed to target hardware implementations, specifically field-programmable gate arrays (FPGAs) [PL91, SP93, BHP94, LKL $\left.{ }^{+} 95\right]$. The language can be viewed as a pointer-free subset of C, augmented with a parallel construct and chan-
nel communication, as found in CSP. The type system has been modified to explicitly refer to the number of bits required to implement any given type. The language targets synchronous hardware with a single master clock. All assignments and channel communication events take one clock cycle, with all updates synchronised with the clock edge marking the cycle end. All expression and conditional evaluation (for selection, loops) is deemed to take 'zero-time', effectively being completed before the current clock cycle ends.
In order to facilitate work on a formal semantics for Handel-C, it was decided at first to consider a subset only, namely the smallest subset that would catch all the essential and potentially difficult aspects. After discussions with Ian Page, a suitable subset of Handel-C was identified. It includes channels, assignment, conditional, one loop, and the parallel construct as well as shared variables. Key omissions include types, macros, shared expressions, RAMs, ROMs, arrays and the bus interface material, all of which can be handled fairly straightforwardly, in semantic terms. Discussing requirements for a set of algebraic laws with Jim Woodcock, in particular with respect to normal forms, it was decided to include the 'prioritised alternatives' construct (prialt) in the reduced syntax. Another key motivation for having the prialt is that it is the only mechanism for introducing non-blocking (or try-else-skip) synchronisations into programs.
This report presents an interpretive semantics of a prialt-free subset of the Handel-C hardware compilation language. The language is presented here with such a construct, but its semantics are not fully described at this point. This is due in the main to the lack of clear documentation on the behaviour of this construct, which will require some experimentation in order to be able to elucidate an appropriate semantics. This has been left as a future task. However, the semantics presented here has been defined with prialt in mind, which explains why the semantics of communication appears more complicated than it needs to be.
This document presents the semantics, but does not provide any formal or informal validation. This will be the subject of another technical report.
In $\S 2$ we introduce the abstract syntax, which we the augment with an explicit environment mapping in $\S 3$. Given these, in $\S 4$ we present the 'control-flow' or interpretive semantics.

## 2 Reduced Abstract Syntax

We present an abstract form of the reduced form of the Handel-C language, in the form of an abstract data type capturing the essential syntactical structure. This is based on chapter 9 of the Handel-C Language Reference Manual, v2.1 [Emb], as indicated in the section headings.

### 2.1 Program (§9.3.17, p197)

$$
\operatorname{Prg} \quad::=\quad \text { InitDecl }{ }^{\star} \times \text { Decl }^{\star} \times \text { Stmnt }
$$

### 2.2 Declarations (§9.3.6, p189)

Identifiers ( $I d$ ) in this subset play three rôles, naming variables ( $I d_{v}$ ), channels $\left(I d_{c}\right)$ and (builtin) operators ( $\left.I d_{o}\right)$. Even though these three uses are syntactically distinguishable, allowing us to treat them as separate name spaces, we prefer to keep one name-space for variables and channels, to avoid having to carry distinct mappings around.

$$
\text { InitDecl }::=I d \times \mathbb{Z}
$$

Local declarations have no initialisers.

| Decl $::=$ | Var Id |  |
| ---: | :---: | :--- |
|  | $\mid$ | Chan Id |
|  |  | ChIn Id |
|  |  | ChOUT Id |

### 2.3 Statements (§9.3.16, pp196-7)

In Handel-C we have the usual assignment (ASG), sequencing (SEQ), conditional (IF), looping (WHL) and jumping (BRK) constructs familiar to imperative language programmers. From CSP, we obtain the parallel construct (PAR) and channel communication primitives for input (In) and output (OUT). Specific to Handel-C is the delay statement (DLY) which does nothing, but takes one full clock-cycle to do it.

$$
\begin{aligned}
\text { Stmnt }::= & \text { SEQ Decl } l^{\star} \text { Stmnt }^{+} \\
& \text {Par Decl } \text { Stmnt }^{2+} \\
& \text { AsG Id Exprn } \\
& \text { In Id Id } \\
: & \text { OUT Id Exprn } \\
& \text { IF Exprn Stmnt }[\text { Stmnt }] \\
& \text { Whl Exprn Stmnt } \\
& \text { Pri PCse } \\
& \text { Brk } \mid \text { Dly } \mid \text { Cont }
\end{aligned}
$$

The Cont (continue) statement is not in Handel-C, but is provided here as a technical device to facilitate translation into the explicit environment syntax form.

## 2.4 prialt Cases (§9.3.16, p197)

The prialt statement is a sequence of communication requests (Cin, Cout), each with an associated statement that is executed afterwards, if the corresponding communication succeeds. A prialt statement may have a final 'default' clause,
which always succeeds

| PCse $::=$ | Cin Id Id Stmnt |  |
| ---: | :--- | ---: | :--- |
|  |  | Cout Id Exprn Stmnt |
|  |  | Default Stmnt |

The ordering of the prialt clauses is significant in that it assigns a priority to each request.

### 2.5 Expressions (§9.3.15, pp194-5)

Handel-C has the usual range of expressions, as well as ones tailored more closely to its hardware targeting rôle. However the details are not really of any immediate interest here. Instead we simply assume a simple epxression grammar built up from numbers (NUM), variables (VAR) and applications (APP) of builtin operators and functions

$$
\begin{array}{rll}
\text { Exprn } & ::= & \text { NUM } \mathbb{Z} \\
& \mid & \text { VAR Id } \\
& \mid & \text { APP Id Exprn }
\end{array}
$$

### 2.6 Identifier (§9.3.1, p186)

Identifiers are just a countable type with equality defined, typically represented by character sequences

$$
I d \quad::=\mathbb{A}^{+}
$$

### 2.7 Abstract Syntax Summary

$$
\begin{aligned}
& \text { Prg ::= InitDecl }{ }^{\star} \times \text { Decl }^{\star} \times \text { Stmnt } \\
& \text { InitDecl ::= } I d \times \mathbb{Z} \\
& \text { Decl ::= VAR Id } \\
& \text { - Chan Id } \\
& \text { | ChIn Id } \\
& \text { | ChOut Id } \\
& \text { Stmt ::= SEQ Decl }{ }^{\star} \text { Stmnt }^{+} \\
& \text {- Par Decl }{ }^{\star} \text { Stmnt }^{2+} \\
& \text { | Asg Id Exprn } \\
& \text { | In Id Id } \\
& \text { | Out Id Exprn } \\
& \text { | If Exprn Stmnt [Stmnt] } \\
& \text { | Whl Exprn Stmnt } \\
& \text { | Pri PCse* } \\
& \text { | Brk } \mid \text { Dly } \mid \text { Cont } \\
& \text { PCse ::= Cin } I d_{c} \text { Id } d_{v} \text { Stmnt } \\
& \text { | Cout } I d_{c} \text { Expr Stmnt } \\
& \text { | Default Stmnt } \\
& \text { Expr ::= Num } \mathbb{Z} \\
& \text { | Var Id } \\
& \text { | App Id Expr }{ }^{+} \\
& \text {Id }::=\mathbb{A}^{+}
\end{aligned}
$$

## 3 Explicit Environment Abstract Syntax

As all program variables are statically allocated (there are no function/procedure call frames in Handel-C), we convert all declarations into an environment mapping, which maps tagged identifiers to their values. The tags are tree cursors (natural number sequences) that identify the point in the abstract syntax tree where the declaration occurred.

### 3.1 Trees

### 3.1.1 Tree Type

We shall assume all trees are expressed in the general form

$$
(a, \tau), T \in \operatorname{Tree} A \widehat{=} A \times(\text { Tree } A)^{\star}
$$

### 3.1.2 Tree Access Functions

We have functions to access the node $(\mathcal{N})$, determine the number of sub-trees (\#) and to access the $i$ th sub-tree ( $\quad . i$ ):

$$
\begin{aligned}
\mathcal{N} & : \text { Tree } A \rightarrow A \\
\mathcal{N}(a,-) & \widehat{=} \\
\# & : \text { Tree } A \rightarrow \mathbb{N} \\
\#(-, \tau) & \widehat{=} \operatorname{len} \tau \\
-\cdot- & :(\text { Tree } A) \times \mathbb{N}_{1} \rightarrow \text { Tree } A \\
\operatorname{pre-}(T . i) & \widehat{=} i \leq \# T \\
(-, \tau) . i & \widehat{=} \tau[i]
\end{aligned}
$$

### 3.2 Tree Cursors

### 3.2.1 Tree Cursor Type

A tree cursor $(T C)$ is a sequence of non-zero natural numbers:

$$
\kappa \in T C \widehat{=} \mathbb{N}_{1}^{\star}
$$

A tree cursor denotes the path to be followed from the root mode to the indicated node, by identifying number of each successive sub-tree to be followed. An empty sequence denotes the root, while a non-empty sequence gives the subtree indices in reverse order. For example, the sequence $\langle 3,5,2\rangle$ denotes the 3 rd sub-tree of the 5 th sub-tree of the 2 nd sub-tree of the root. This reversal is done to make the operations of moving to a parent or child simpler, in that they can be directly expressed as head and tail operations.

### 3.2.2 Manipulating Tree Cursors

A Tree cursor can be modified to refer to the parent or the $i$ th child of the tree node it denotes, provided, of course this is defined.

$$
\begin{array}{rcl}
-\uparrow & : & T C \rightarrow T C \\
\operatorname{pre}-(\kappa \uparrow) & \widehat{=} & \kappa \neq \Lambda \\
\kappa \uparrow & \widehat{=} & \mathrm{tl} \kappa \\
-\downarrow- & T C \times \mathbb{N}_{1} \rightarrow T C & \\
\kappa \downarrow i & \widehat{=} & i: \kappa
\end{array}
$$

### 3.2.3 Tree Lookup

We can apply a cursor to a tree to find the relevant sub-tree node. This is only defined if the cursor matches the tree structure:

$$
\begin{aligned}
-(-) & : \text { Tree } A \rightarrow \text { Cursor } \rightarrow \text { Tree } A \\
\operatorname{pre-T(\Lambda )} & \widehat{=} \text { TruE } \\
\operatorname{pre}_{-}(-, \tau)(\kappa \frown\langle i\rangle) & \widehat{=} i \leq \operatorname{len} \tau \wedge \operatorname{pre-}(\tau[i])(\kappa) \\
T(\Lambda) & \widehat{=} \\
(-, \tau)(\kappa \frown\langle i\rangle) & \widehat{=} \tau[i](\kappa)
\end{aligned}
$$

### 3.3 Tree Environments

### 3.3.1 Cursor-Tagged Identifiers

We tag identifiers by pairing them with a tree cursor denoting the tree node where the identifier was declared:

$$
C t I d \hat{=} T C \times I d
$$

### 3.3.2 Tree Environment Type

A Tree Environment (TEnv $V$ ) is a (finite) mapping from cursor tagged identifiers to values drawn from type $V$ :

$$
\rho \in T E n v V \quad \widehat{=} \quad C t I d \rightarrow V
$$

### 3.3.3 Tree Environment Lookup/Update

As we are recording all identifiers in the explicit syntax with cursor tags indicating their point of declaration, we can use simple map application and override to achieve lookup and update.

### 3.3.4 Tree Environment Key Lookup

We need to take an identifier tagged with its point of use, and look it up in the environment in order to re-tag it with its point of declaration:

$$
\begin{aligned}
\text { key } & : \quad \text { TEnv } V \rightarrow C t I d \rightarrow C t I d \\
\operatorname{key}[\rho](\kappa, i) & \widehat{=} \quad(\kappa, i) \in \operatorname{dom} \rho \rightarrow(\kappa, i), \operatorname{key}[\rho](\kappa \uparrow, i)
\end{aligned}
$$

The use of this function in the sequel depends on variables being declared before use, in the ordering resulting from an prefix traversal of the syntax tree.

### 3.4 Variable Data and Channel States

We need to define the data values and the channel states that our semantics will need. Ordinary variables simply have a data value, whereas channel variables
have channel states, as well as pointers to the reading and writing statements and channel data values

| ValState $::=$ | Val Data |  |
| ---: | :--- | :--- |
|  |  | Chan ChState TC TC |
|  | InCh ChState TC |  |
|  |  | OutCh ChState TC |
|  |  | Stop $\mid$ Go |

We will use null (root) tree cursors here to denote a null value, rather than the root. This makes sense because the tree root (entire program) can never be a channel statement. A channel's state is either idle, or waiting for either input or output, or ready:

$$
\text { ChState } \widehat{=} \text { Idle } \mid \text { Wait } \mid \text { Input } \mid \text { Output } \mid \text { REady } \mid \text { Error }
$$

Data values (Data) are currently integers, extended with an unknown value (?):

$$
\text { Data } \widehat{=} \mathbb{Z} \cup\{?\}
$$

We shall define our program environment (Env) to be a ValState tagged environment:

$$
\text { Env } \widehat{=} \text { TEnv ValState }
$$

### 3.5 Explicit Environment Syntax

We are going to replace an abstract syntax based on $\operatorname{Prg}$ by one based on a new syntax tree based on a reworked definition of Stmnt.

### 3.5.1 Statements revisited

We discard any declarations (to be replaced by a single tree environment) and the sub-tree components, tag all identifiers, and introduce a continue and generalised delay statements:

$$
\begin{array}{rll}
\text { Stmt }::= & \text { SEQ } \mid \text { Par } \\
& \text { AsG CtId Expr } \\
\mid & \text { In CtId CtId } \\
: & \text { Out CtId Expr } \\
\mid & \text { If Expr } \\
: & \text { Whl Expr } \\
\mid & \text { Pri } \\
\mid & \text { Brk } \mid \text { Cont } \\
& \text { Dly } \mathbb{N}
\end{array}
$$

### 3.5.2 prialt Cases revisited

The prialt cases now become sequential statements whose first sub-statement is the synchronisation statement.

### 3.5.3 Expression revisited

We redefine expressions to use CtIds intead of Ids:

$$
\begin{aligned}
\text { Expr }:::= & \text { Num } \mathbb{Z} \\
& \mid \\
& \text { VAR CtId } \\
& \text { App CtId Expr }
\end{aligned}
$$

We can define the evaluation of an expression in a given environment as

$$
\begin{aligned}
-(-) & : \quad \text { Expr } \rightarrow \text { Env } \rightarrow \text { Data } \\
\rho(\operatorname{NUM} n) & \hat{=} n \\
\rho(\operatorname{VAR} v) & \widehat{=} n \text { where }(\operatorname{VAL} n)=\rho(v) \\
\rho(\operatorname{APP} f \sigma) & \hat{=} \llbracket f \rrbracket \circ \rho^{\star} \sigma
\end{aligned}
$$

We have overloaded the map application notation. Which is meant is determined by the type of the operand, if an expression, it is what has just been defined, if a tagged identifier, it is regular map lookup. Here $\llbracket f \rrbracket$ gives the fixed meaning of $f$ which is builtin.

### 3.5.4 Abstract Syntax Tree revisited

We then define our new abstract syntax as a tree over Stmt with an invariant that restricts the length $(\ell=\# T)$ of the sub-tree sequence:

$$
\begin{aligned}
& \text { Stmt } T \hat{=} \text { Tree Stmt } \\
& \text { inv-StmtT SEQ } \hat{=} \ell>0 \\
& \text { inv-StmtT PAR } \hat{=} \quad \ell>1 \\
& \operatorname{inv-StmtT} \text { (IF }-) \hat{=} \quad \ell \in 1,2 \\
& \text { inv-StmtT (Whl _) } \hat{=} \quad \ell=1 \\
& \text { inv-StmtT PRI } \hat{=} \ell>0 \\
& \text { inv-StmtT } T \hat{=} \quad \ell=0
\end{aligned}
$$

Not shown here is that fact that the invariant is applied recursively to all subtrees.

### 3.5.5 Program revisited

Our new program representation is a new abstract syntax tree coupled with a tree environment:

$$
\text { Prog } \hat{=} \operatorname{Stmt} T \times E n v
$$

Note that a program is now any statement with an associated environment. Note also that strictly speaking we need an invariant that states that all cursors in the domain of the environment (as tags) must be compatible with the statement tree. By compatible, we mean that they refer to actual tree nodes, and being fully pedantic, that they only indicate SEQ or PAR nodes, as these are the only ones to have declarations in the original Handel-C.

### 3.6 Build the Explicit Environment Tree

We need to convert a program from Prg to Prog form. We do this by creating an initial environment, and traversing the original tree with a cursor tracking our position, converting declarations to tagged environment entries, and building the new tree.

### 3.6.1 Converting $\operatorname{Prg}$

We use the program's declarations to build an initial environment, tagged with the null (root) cursor. The program main statement becomes the sole sub-tree of a (top-level) SEQ node:

$$
\begin{aligned}
\operatorname{cnvPrg}: & \operatorname{Prg} \rightarrow \operatorname{Prog} \\
\operatorname{cnvPrg}\left(\imath, \delta, s_{0}\right) \quad \widehat{=} & \operatorname{cnvStmt}\left(\langle 1\rangle, \rho_{0}\right) s_{0} \\
& \text { where } \rho_{0}=\operatorname{cnvInitDecls} \imath \dagger \operatorname{cnvDecls}[\Lambda] \delta
\end{aligned}
$$

### 3.6.2 Converting InitDecl

Initial Declarations simply become mappings from root-tagged identifiers to their initial values:

$$
\begin{aligned}
\text { cnvInitDecl } & : \text { InitDecl } \rightarrow \text { Env } \\
\text { cnvInitDecl }\left(v, i_{0}\right) & \widehat{=}\left\{(\Lambda, v) \mapsto\left(\operatorname{VAL} i_{0}\right)\right\} \\
& \\
\text { cnvInitDecls } & : \text { InitDecl }{ }^{\star} \rightarrow \text { Env } \\
\text { cnvInitDecls } \Lambda & \widehat{=} \theta \\
\text { cnvInitDecls }(i: \imath) & \widehat{=}(\text { cnvInitDecl } i) \dagger(\text { cnvInitDecls } \imath)
\end{aligned}
$$

Note that later declarations supersede earlier ones. In Handel-C multiple declarations of the same identifier in the same scope are illegal, so this is not an issue. We simply take this approach using override to keep the semantics simple.

### 3.6.3 Converting Decl

$$
\begin{aligned}
\operatorname{cnvDecl} & : T C \rightarrow \text { Decl } \rightarrow \text { Env } \\
\operatorname{cnvDecl}[\kappa]\left(\mathrm{VAR}^{\prime} v\right) & \widehat{=}\{(\kappa, v) \mapsto \operatorname{VAL} ?\} \\
\operatorname{cnvDecl}[\kappa]\left(\mathrm{CHAN}^{\prime} c\right) & \widehat{=}\{(\kappa, c) \mapsto \operatorname{ChAN} \operatorname{IdLE} \Lambda \Lambda\} \\
\operatorname{cnvDecl}[\kappa]\left(\mathrm{CHIN}^{\prime} c\right) & \widehat{=}\{(\kappa, c) \mapsto \operatorname{ChIn} \operatorname{IdLE} \Lambda\} \\
\operatorname{cnvDecl}[\kappa]\left(\mathrm{CHOUT}^{\prime} c\right) & \widehat{=}\{(\kappa, c) \mapsto \operatorname{ChOUT} \operatorname{IdLE} \Lambda\} \\
\operatorname{cnvDecls} & : T C \rightarrow D e c l^{\star} \rightarrow E n v \\
\operatorname{cnvDecls}[\kappa] \Lambda & \widehat{=} \theta \\
\operatorname{cnvDecls}[\kappa](d: \delta) & \widehat{=}(\operatorname{cnvDecl}[\kappa] d) \dagger(\operatorname{cnvDecls}[\kappa] \delta)
\end{aligned}
$$

## 3．6．4 Converting Stmt

We convert a statement on a case－by－case basis：

```
cnvStmt : TC ×Env }->\mathrm{ Stmnt }->\mathrm{ StmtT }\timesEn
    cnvStmt[ }\kappa,\rho](\operatorname{SEQ}\delta\sigma
        = let }\mp@subsup{\rho}{}{\prime}=\rho\sqcup\operatorname{cnvDecls[\kappa]}
        in let ( }\tau,\mp@subsup{\rho}{}{\prime\prime})=\operatorname{cnvStmts}[\kappa,\mp@subsup{\rho}{}{\prime}]\sigma\mathrm{ in ((SEQ, }),\mp@subsup{\rho}{}{\prime\prime}
    cnvStmt[ }\kappa,\rho](\operatorname{PaR}\delta\sigma
        = let }\mp@subsup{\rho}{}{\prime}=\rho\sqcup\operatorname{cnvDecls}[\kappa]\delta\sqcup{(\kappa,_t)\mapsto\operatorname{VAL}0
            in let (\tau, 少) = cnvStmts[ }\kappa,\mp@subsup{\rho}{}{\prime}]\sigma\mathrm{ in ((PAR, 
    cnvStmt[\kappa,\rho](ASG ve)
        \hat{=}}((\operatorname{ASG}(\operatorname{key}[\rho](\kappa,v))e,\Lambda),\rho
    cnvStmt[\kappa,\rho](\operatorname{IN cv})
        \hat{=}}((\operatorname{IN}(\operatorname{key}[\rho](\kappa,c))(\operatorname{key}[\rho](\kappa,v)),\Lambda),\rho
    cnvStmt[ }\kappa,\rho](\mathrm{ OUT ce)
        \hat{=}}((\operatorname{OUT}(\operatorname{key}[\rho](\kappa,c))e,\Lambda),\rho
    cnvStmt[\kappa, \rho](IFe
        \widehat { = } \operatorname { l e t } ( T _ { 1 } , \rho _ { 1 } ) = \operatorname { c n v I x S t m t } [ \kappa , \rho ] ( 1 , s _ { 1 } )
            in let }(\mp@subsup{T}{2}{},\mp@subsup{\rho}{2}{})=\operatorname{cnvIxStmt}[\kappa,\rho](2,\mp@subsup{s}{2}{})\operatorname{in}((\operatorname{IF}e,\langle\mp@subsup{T}{1}{},\mp@subsup{T}{2}{}\rangle),\mp@subsup{\rho}{1}{}\cup\mp@subsup{\rho}{2}{}
    cnvStmt[ [ , \rho](IF e s)
    \widehat{= let }(\mp@subsup{T}{}{\prime},\mp@subsup{\rho}{}{\prime})=\operatorname{cnvIxStmt[ [\kappa,\rho] (1,s) in ((IF e,\langleT'}\rangle),\mp@subsup{\rho}{}{\prime})
    cnvStmt[\kappa, \rho](WHL es)
        \widehat{= let }(\mp@subsup{T}{}{\prime},\mp@subsup{\rho}{}{\prime})=\operatorname{cnvIxStmt}[\kappa,\rho](1,s)\mathbf{in}((\mathrm{ WhL }e,\langle\mp@subsup{T}{}{\prime}\rangle),\mp@subsup{\rho}{}{\prime})
    cnvStmt [ }\kappa,\rho](\mathrm{ PRI }\varpi
        \widehat{= let}(\mp@subsup{\tau}{}{\prime},\mp@subsup{\rho}{}{\prime})=\operatorname{cnvStmts}[\kappa,\rho](\operatorname{cnvPCase}[\kappa\mp@subsup{]}{}{\star}\varpi)
            in ((PRI, 看), 生\sqcup{(\kappa,_s)\mapstoSTOP })
    cnvStmt[ }\kappa,\rho](\textrm{BRK}
        = ((BRK, \Lambda),\rho)
    cnvStmt[ }\kappa,\rho]\mathrm{ (DLY)
        = ((DLY 1,\Lambda),\rho)
    cnvStmt[ }\kappa,\rho](\mathrm{ Cont)
        =}((\textrm{CoNT},\Lambda),\rho
```

We provide a form for converting the $i$ th sub－statement directly－this is used above，as well as below for handling sequences of sub－statements：

$$
\begin{aligned}
\operatorname{cnv\operatorname {IxStmt}} & : T C \times E n v \rightarrow \mathbb{N}_{1} \times S t m n t \rightarrow S t m t T \times E n v \\
\operatorname{cnvIxStmt}[\kappa, \rho](i, s) & \widehat{=} \operatorname{cnvStmt}[i: \kappa, \rho] s
\end{aligned}
$$

Converting statement sequences simply involves generating appropriate indices and then zipping these with the statements，mapping indexed conversion across
and unzipping to get the result:

$$
\begin{array}{rcl}
\operatorname{cnvStmts} & : & T C \times E n v \rightarrow \text { Stmnt }^{+} \rightarrow \text { Stmt }^{+} \times E n v \\
\operatorname{cnvStmts}[\kappa, \rho] \sigma & \widehat{=} & \left(\tau, \rho^{\prime}\right) \\
& \text { where } \\
& \imath=\langle 1 \ldots \text { len } \sigma\rangle \\
& (\tau, \varrho)=\left(\operatorname{unzip} \circ\left(\operatorname{cnvIxStmt}[\kappa, \rho]^{\star} \circ \operatorname{zip}\right)(\imath, \sigma)\right. \\
& \rho^{\prime}=\cup \varrho
\end{array}
$$

### 3.6.5 Converting PCase

Converting prialt cases mainly involves stripping out the statements and converting the channel information to statements and returning those contained in a sequential compound statement

$$
\begin{aligned}
\text { cnvPCase } & : T C \rightarrow P C s e \rightarrow \text { Stmnt } \\
\operatorname{cnvPCase}[\kappa](\operatorname{Cin} c v s) & \widehat{=} \operatorname{SEQ} \Lambda\langle\operatorname{In}(\operatorname{key}[\rho](\kappa, c))(\operatorname{key}[\rho](\kappa, v)), s\rangle \\
\operatorname{cnvPCase}[\kappa](\operatorname{CoUT} \text { ce } s) & \widehat{=} \operatorname{SEQ} \Lambda\langle\operatorname{OUT}(\operatorname{key}[\rho](\kappa, c)) e, s\rangle \\
\operatorname{cnvPCase}[\kappa](\text { DEFAULT } s) & \widehat{=} \operatorname{SEQ} \Lambda\langle\operatorname{ConT}, s\rangle
\end{aligned}
$$

## 3．7 Explicit Environment Syntax Summary

```
            Prog \hat{= StmtT }\timesEnv
            StmtT \widehat{= Tree Stmt}
    inv-StmtT SEQ \hat{= }\quad\ell>0
    inv-StmtT PAR 人
    inv-StmtT (IF _) 人 =}\quad\ell\in0,
inv-StmtT(WHL _) \hat{=}\quad\ell=1
    inv-StmtT PRI 人}=\ell>
        inv-StmtT T 人}\quad\ell=
            Stmt ::= SEQ|PAR
            | Asg CtId Expr
            In CtId CtId
            | Out CtId Expr
            IF Expr
            WHL Expr
            | Pri
            | Brk | Cont
            | Dly N
            Expr ::= Num\mathbb{Z}
                    | VAR CtId
                        | App CtId Expr +
                    Id ::= A}\mp@subsup{\mathbb{A}}{}{+
                Env \hat{= CtId }->\mathrm{ ValState}
            ValState ::= VAL Data
            | Chan ChState TC TC
            | InCh ChState TC
            | OutCh ChState TC
                        | Stop | Go
            Data \widehat{=}\mathbb{Z}\cup{?}
                ChState \widehat{= Idle | Wait | Input | Output | Ready | Error}
```


## 4 Control Flow (Interpretative) Semantics

### 4.1 Introduction

We shall model control flow by the notion of "program counters" or "execution pointers" which are simply tree cursors. In general, because of the parallelism, we will have a set of active execution pointers. If this set becomes empty, then the program has terminated. Program execution involves three conceptual phases:

- Execution Pointer Setup The execution pointers are updated to point to the next set of atomic statements to be executed. This phase starts and ends with pointers pointing only at atomic statements. Pointers may indicate composite statements during this process. This phase is in fact split into two sub-phases. The prialt and communication statements require global knowledge to be fully handled, so the second phase, is used to finalise things once all the other active atomic statements have been identified.
- Right-hand Side evaluation The right-hand side expressions of all the selected assignments and channel statements are evaluated.
- Left-hand Side Assignment The left-hand variables and channel contents are then simultaneously updated.

These three phases comprise one clock cycle. The third phase coincides with the assignment clock edge.

### 4.2 Running the Program

We take a program, termination predicate and (initial) environment, determine the first execution pointers and then repeatedly cycle, until there are either no more execution pointers, or the termination predicate returns true. The program state is the current set of execution pointers and the environment:

$$
\Sigma,(K, \rho) \in \text { PState }=\mathcal{P} T C \times E n v
$$

The termination predicate is over program state:

$$
\mho \in \text { TermPred } \widehat{=} \text { PState } \rightarrow \mathbb{B}
$$

We return the program state at the time of termination.

$$
\begin{aligned}
\operatorname{cfBeh}: & \text { TermPred } \rightarrow \text { Prog } \rightarrow \text { PState } \\
\operatorname{cfBehStart}_{\mho}\left(T, \rho_{0}\right) \widehat{=} & \text { repeat cycle }\left(\text { fstep }_{T}(\rho) \Lambda\right) \mho^{\prime} \\
& \text { where } \\
& \mho^{\prime}(K, \rho) \widehat{=} \mho(K, \rho) \vee K=\emptyset
\end{aligned}
$$

The most useful purpose for the predicate is to allow either 'single-stepping' or stopping at external (observable) communications events. Such a stopped
program can be restarted by:

$$
\begin{aligned}
\text { cfBehResume } & : \text { TermPred } \rightarrow \text { Stmt } T \rightarrow \text { PState } \rightarrow \text { PState } \\
\text { pre-cfBehResume } & {[T](K, \rho) } \\
\widehat{=} & K \neq \emptyset \\
\operatorname{cfBehResume} & {[T](K, \rho) } \\
& \text { repeat cycle }(K, \rho) \mho^{\prime} \\
& \text { where } \\
& \mho^{\prime}(K, \rho) \widehat{=} \mho(K, \rho) \vee K=\emptyset
\end{aligned}
$$

The precondition is not strictly necessary as cycle acts like an identity function if $K$ is empty.

### 4.3 The Clock Cycle

We take the program, environment and set of execution pointers and return a modified environment and set of next execution pointers. We carry out the three phases in the order: evaluate rhs, assign lhs, and determine next execution pointers:

$$
\begin{array}{rc}
\text { cycle } \quad & \text { Stmt } \rightarrow \text { PState } \rightarrow \text { PState } \\
\operatorname{cycle}_{T}(K, \rho) \widehat{=} & \text { let } \rho_{r}=\operatorname{evalRhs}_{T, \rho}[\theta] K \text { in } \\
& \text { let } \rho_{c}=\operatorname{evalComms}_{T, \rho}[\theta] \rho \text { in } \\
& \text { let } \rho_{l}=\rho \dagger\left(\rho_{r} \sqcup \rho_{c}\right) \text { in } \\
& \operatorname{incExePtrs}_{T}\left(K, \rho_{l}\right)
\end{array}
$$

The reason for this ordering is that we call cycle with an initial set of execution pointers.

### 4.3.1 Evaluating the RHS

For assignments we simply evaluate the rhs in the current environment and set the variable to that value in the new environment:

$$
\begin{aligned}
& \text { evalRhs }: \\
& \operatorname{evalmt}^{\prime} \times E n v \rightarrow E n v \rightarrow \mathcal{P} T C \rightarrow E n v \\
& \operatorname{evalRhs}_{T, \rho}\left[\rho^{\prime}\right]\left(\rho^{\prime}\right] \emptyset \cup\{ \widehat{=} \rho^{\prime} \\
& \text { where } \\
& \rho^{\prime \prime} \widehat{=} T(\kappa)=(\text { AsG } v e) \rightarrow\{v \mapsto \operatorname{VAL} \rho(e)\}, \theta
\end{aligned}
$$

For communication statements we simply evaluate the output rhs in the current environment and set the input variable to that value in the new environment. We work through the environment, rather than the execution pointers because
we need to pick up both statements for each channel:

```
evalComms
    : StmtT \(\times E n v \rightarrow E n v \rightarrow E n v \rightarrow E n v\)
\(\mathrm{evalComms}_{T, \rho}\left[\rho^{\prime}\right] \theta\)
    \(\widehat{=} \rho^{\prime}\)
evalComms \(T_{, \rho}\left[\rho^{\prime}\right](\mu \sqcup\{c \mapsto s\})\)
    \(\widehat{=} \operatorname{evalComms}_{T, \rho}\left[\rho^{\prime} \dagger \rho^{\prime \prime}\right] \mu\)
            where
                \(\rho^{\prime \prime} \widehat{=} \operatorname{stateOf} \rho(c)=\operatorname{READY} \rightarrow \operatorname{evalComm}[T, \rho](\rho(c)), \theta\)
```

evalComm

```
    : StmtT \(\times\) Env \(\rightarrow\) ValStateEnv
evalComm \([T, \rho]\) (Chan Ready \(\kappa_{i} \kappa_{o}\) )
    \(\widehat{=} \operatorname{let}\left(\right.\) OUT \(\left._{-} e\right)=\pi_{1}\left(T\left(\kappa_{o}\right)\right)\)
        and \(\left(\mathrm{IN}_{-} v\right)=\pi_{1}\left(T\left(\kappa_{i}\right)\right)\) in
            \(\rho \dagger\{v \mapsto \operatorname{VAL} \rho(e)\}\)
```


### 4.3.2 'Incrementing' Execution Pointers

We apply nxtep to all existing pointers to get new ones, factor out the communication pointers, use resep to resolve them, and merge the outcome back in:

$$
\begin{array}{rc}
\operatorname{incExePtrs}: & \text { Stmt } T \rightarrow \text { PState } \rightarrow \text { PState } \\
\operatorname{incExePtrs}_{T}(K, \rho) & \widehat{=} \\
& \text { let }\left(K^{\prime}, \rho^{\prime}\right)=\text { foldNxt }_{T}\left(\emptyset, \rho^{\prime}\right) K^{\prime} \text { in } \\
& \text { let }\left(K_{a}, K_{p}\right)=\left(\varangle\left[P_{T}\right] K^{\prime}, \triangleleft\left[P_{T}\right] K^{\prime}\right) \text { in } \\
& \operatorname{let}\left(K_{c}, \rho^{\prime \prime}\right)=\operatorname{resep}_{T}\left(\rho^{\prime}\right) K_{p} \text { in } \\
& \left(K_{a} \cup K_{c}, \rho^{\prime \prime}\right) \\
& \text { where } \\
& P_{T}(\kappa) \widehat{=} \pi_{1}(T(\kappa))=\text { PRI } \\
& \vee \pi_{1}(T(\kappa))=\left(\text { IN_ }_{-}\right) \\
\vee \pi_{1}(T(\kappa))=\left(\text { OUT_ }_{-}\right)
\end{array}
$$

This function simply applies nxtep to a set of execution pointers, passing the environment between them.

$$
\begin{aligned}
\text { foldNxt } & : \text { StmtT } \rightarrow \text { PState } \rightarrow \mathcal{P} T C \rightarrow \text { PState } \\
\operatorname{foldNxt}_{T}(K, \rho) \emptyset \widehat{=} & (K, \rho) \\
\text { foldNxt }_{T}\left(K_{a}, \rho\right)\left(K_{p} \sqcup\{\kappa\}\right) \widehat{=} & \text { foldNxt }_{T}\left(K_{a} \cup K^{\prime}, \rho^{\prime}\right) K_{p} \\
& \text { where } \\
& \left(K^{\prime}, \rho^{\prime}\right)=\operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

This function is defined recursively over sets, so it is potentially non-deterministic. We need to show it actually is a function. The key to such a proof is that multiple execution pointers only result for PAR statements, and their sole interaction
is to increment and decrement the thread count for the PaR statement (§4.5.4), in a manner that is independent of the order in which each thread is created or destroyed.

### 4.4 Determining Next Execution Pointers

The process of computing the next execution pointers involves the following functions:

- fstep (first execution point) - given an execution pointer pointing to a statement, this identifies the first atomic statements to be executed in that statement, relative to the current state of the environment. In general it may return zero, one, or more pointers, as well as modifying the environment (or that portion of it which deals with synchronisation). For Pri statements, it returns a pointer to the statement itself, and not an atomic sub-statement.
- nxtep (next execution point) - given an execution pointer pointing to a statement, this seeks to identify the next atomic statement to be executed, by looking at the parent of the current statement. As above, it will refer to the current environment, and it may return zero, one, or more pointers, and a modified environment.
- resep (resolve communication execution points) - given execution pointer for all active communication statements, it determines, by looking at the state of the channels involved, which are going to execute.

The function fstep is applied to the program (root statement) at the beginning to establish the first execution points. The first phase simply applies nxtep to every current execution point, then applies resep to all selected communication statements, and combines the results.

### 4.5 Definitions of fstep and nxtep

We shall start by giving a general overview of the function signatures and their first activity at the top-level. Then we present the behaviour for each statement type on a case by case basis.
Both functions take a program tree, environment, and execution pointer as arguments. Both return a set of execution pointers and an environment as results:

$$
\begin{aligned}
& \text { fstep }: \quad S t m t T \rightarrow E n v \rightarrow T C \rightarrow \text { PState } \\
& \text { nxtep }: \quad S t m t T \rightarrow E n v \rightarrow T C \rightarrow \text { PState }
\end{aligned}
$$

The program tree $(T)$ is a fixed parameter, designating the entire program, and does not change.
The fstep function uses the execution pointer to lookup the current statement for case analysis. If the statement is atomic or a prialt then we are done, and we
return that execution pointer. With the prialt we also set it's status to Stop
:

The other statement types will be dealt with below on a case-by-case basis. We shall use the form

$$
\text { fstep }_{T}(\rho) \kappa \quad \text { where } \quad T(\kappa)=\ldots
$$

to denote the cases concerned.
The nxtep function uses part of the execution pointer to lookup the current statement's parent for case analysis, except for communications statements, discussed shortly. If the execution pointer is empty, then we are asking for the next statement after the program as a whole, so we respond with an empty pointer set, signalling program termination:

$$
\operatorname{nxtep}_{T}(\rho) \Lambda \hat{=}(\emptyset, \rho)
$$

Otherwise, we determine if we have a communication statement that is blocked, in which case we return their execution pointers. Otherwise, we do case analysis on the parent, which must therefore be composite:

```
\(\operatorname{nxtep}_{T}(\rho)(i: \kappa)\)
    \(\widehat{=} \quad \operatorname{case} T(i: \kappa)\)
                \((\operatorname{In} c v, \Lambda) \mid \operatorname{stateOf}(\rho(i: \kappa, c))=\operatorname{InPuT}\)
                \(\rightarrow(\{i: \kappa\}, \rho)\)
            (Out \(c e, \Lambda) \mid \operatorname{stateOf}(\rho(i: \kappa, c))=\) Output
                \(\rightarrow(\{i: \kappa\}, \rho)\)
            \((\mathrm{PRI}, \sigma) \mid \rho\left(i: \kappa, \_\mathbf{s}\right)=\mathrm{STOP}\)
            \(\rightarrow(\{i: \kappa\}, \rho)\)
            otherwise \(\rightarrow\) case \(T(\kappa)\)
```

The function stateOf is :

```
            stateOf : ValState }->\mathrm{ ChState
    stateOf(Chan s _) \widehat{= s}
    stateOf(INCH s _) \hat{= s}
stateOf(OutCh s _) \widehat{=}}
```

The remaining statement types will be dealt with below on a case-by-case basis. We shall use the form

$$
\operatorname{nxtep}_{T}(\rho)(i: \kappa) \quad \text { where } \quad T(\kappa)=\ldots
$$

to denote the cases concerned.
All the cases involve the composite statements. In general we assume that sub-statements are numbered 1 to $N$ where $N$ is the length of their sequence $(N=\operatorname{len} \sigma)$.

### 4.5.1 The Seq Statement

The first execution point of a sequential statement is that of its first substatement:

```
fstep \(_{T}(\rho) \kappa \quad\) where \(\quad T(\kappa)=\left(\operatorname{SEQ},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right)\)
    \(\widehat{=} \operatorname{fstep}_{T}(\rho)(1: \kappa)\)
```

Pictorially, we go from

to


The next execution point of a child of a sequence statement is the first of the next child in sequence, if any more remain. If we are starting from the last child then we look to the sequence's own parent:

$$
\begin{aligned}
& \operatorname{nxtep}_{T}(\rho)(i: \kappa) \quad \text { where } T(\kappa)=\left(\operatorname{SEQ},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \\
& \widehat{=} \quad i<N \rightarrow \operatorname{fstep}_{T}(\rho)(i+1: \kappa) \\
& i=N \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

For all but the last child we go from

to


For the last child we go from

to


### 4.5.2 The If Statement

The first execution point of an if-statement depends on the current value of the condition and is the first execution point of the appropriate sub-statement:

$$
\begin{gathered}
\text { fstep }_{T}(\rho) \kappa \quad \text { where } T(\kappa)=\left(\operatorname{IF} c,\left\langle s_{t} s_{e}\right\rangle\right) \\
\hat{=} \quad \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa) \\
\neg \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(2: \kappa)
\end{gathered}
$$

Pictorially, we go from

to

and from

to


The first execution point of an if-statement with no else-statement depends on the current value of the condition and is the first execution point of the thenstatement if true, otherwise we look for the next statement after the if:

$$
\begin{gathered}
\operatorname{fstep}_{T}(\rho) \kappa \text { where } T(\kappa)=\left(\operatorname{IF} c,\left\langle s_{t}\right\rangle\right) \\
\hat{=} \quad \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa) \\
\neg \rho(c) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa
\end{gathered}
$$

Pictorially, we go from

$$
\begin{gathered}
\text { fstep } \Rightarrow \kappa:: \text { IF TRUE } \\
\downarrow \\
(1: \kappa):: s_{t}
\end{gathered}
$$

to

and from

$$
\begin{gathered}
\text { fstep } \Rightarrow \kappa:: \text { IF FALSE } \\
\downarrow \\
\downarrow \\
(1: \kappa):: s_{t}
\end{gathered}
$$

to


The next execution point of a child of an if-statement is the next point of the parent itself, (regardless of the presence/absence of the else statement):

$$
\begin{aligned}
& \operatorname{nxtep}_{T}(\rho)(i: \kappa) \quad \text { where } \quad T(\kappa)=(\operatorname{IF} c, \sigma) \\
& \quad \widehat{=} \operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

We go from

to


### 4.5.3 The Whl Statement

The first execution point of an while-statement depends on the current value of the condition. If true, it is the first execution point of the body. If false, it is the next execution point after the while-statement.

$$
\begin{aligned}
& \text { fstep }_{T}(\rho) \kappa \text { where } T(\kappa)=\left(\text { WhL } c,\left\langle s_{b}\right\rangle\right) \\
& \hat{=} \quad \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa) \\
& \neg \rho(c) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

Pictorially, we go from

$$
\begin{gathered}
\text { fstep } \Rightarrow \kappa:: \text { WhL TRUE } \\
\downarrow \\
(1: \kappa):: s_{b}
\end{gathered}
$$

to

and from

to


The next execution point of the child of an while-statement is the first execution point of that child if the condition is true, otherwise it is the next execution point of the parent itself:

$$
\begin{aligned}
& \operatorname{nxtep}_{T}(\rho)(1: \kappa) \quad \text { where } T(\kappa)=\left(\text { WhL } c,\left\langle s_{b}\right\rangle\right) \\
& \quad \widehat{=} \quad \rho[c] \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa) \\
& \quad \neg \rho[c] \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

We go from

to

and from

to


The semantics for the WhL statement displays potentially divergent behaviour. If we have a fragment of syntax of the form (annotated with cursors):

$$
\kappa::(\text { Whl True, }\langle 1: \kappa::(\text { Whl False, } \sigma)\rangle)
$$

and we try to compute $\mathrm{ftsep}_{T}(\rho) \kappa$ we obtain:

```
    ftsep
= \langleOuter Whl condition is True\rangle
    ftsep
= \langleInner Whl condition is FalSE\rangle
    nxtep
= \langleOuter Whl condition is True\rangle
    fstep
= \langleInner Whl condition is FalSE\rangle
    nxtep
    \vdots
```

We get an infinite regress ! This is not a disaster, as we have captured an aspect of Handel-C's own behaviour. Handel-C cannot generate the combinatorial logic to compute the next statement in this case. It reports a "combinatorial cycle" at this point. Handel-C issues this as a warning, and then fixes the problem by putting a Dly statement in parallel with the inner while loop. This breaks the conbinatorial cycle, and allows the fstep and nxtep functions to terminate.

### 4.5.4 The Par Statement

The parallel statement is quite complex because (i) we produce multiple threads of execution, each with its own execution pointer, and (ii) we need to keep track of which threads have terminated, as the parallel construct terminates only when all the threads have done so. We use the environment, indexed by a special identifier ( -t ) tagged with the execution pointer for the parallel construct itself,
to store the number of outstanding threads. As control passes into the PaR, the relevant call to fstep updates the environment to reflect the fact that $N$ threads have been started. As each thread terminates, this value is decremented by the corresponding call to nxtep. This is the reason that these functions need to return an environment value.
A technical difficulty could arise here. Some of the sub-statements in the parallel construct could be while-loops with false guards. These will not run and will terminate immediately, so decrementing the thread count. Indeed, if all the bodies are such while-loops, the count will be set to $N$, then decremented $N$ times as the calls to fstep turn into calls to nxtep, finally resulting in nxtep being called on the Par itself! The key point is that the environment needs to be chained among the $N$ calls to fstep for each thread. We cannot simply do the calls 'in parallel'.
We introduce an auxiliary function fstpar which applies fstep to a sequence of statements, and manages the environment threading. The sequence is simply denoted by the number of statements remaining to be processed:

$$
\begin{aligned}
\text { fstpar } & : S t m t T \rightarrow \text { PState } \rightarrow T C \rightarrow \mathbb{N} \rightarrow \mathcal{P} T C \times \text { Env } \\
\operatorname{fstpar}_{T}(P, \rho) \kappa 0 \widehat{=} & (P, \rho) \\
\operatorname{fstpar}_{T}(P, \rho) \kappa(i+1) \widehat{=} & \operatorname{let}\left(P^{\prime}, \rho^{\prime}\right)=\operatorname{fstep}_{T}(\rho)(i+1: \kappa) \\
& \text { infstpar }\left(P \cup P^{\prime}, \rho^{\prime}\right) \kappa i
\end{aligned}
$$

A property we need to check is that the interference between the $N$ applications of fstep is well-behaved.
The first execution point of a parallel statement is that of all its sub-statements:

$$
\begin{aligned}
& \text { fstep }_{T}(\rho) \kappa \quad \text { where } T(\kappa)=\left(\operatorname{PaR},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \\
& \hat{=} \operatorname{fstpar}_{T}\left(\emptyset, \rho \dagger\left\{\left(\kappa, \_\mathrm{t}\right) \mapsto \operatorname{VAL} N\right\}\right) \kappa N
\end{aligned}
$$

Pictorially, we go from

to


Note that fstep should never be applied to a PAR with outstanding threads. This denotes a serious failure of the semantics, as any given Handel-C statement only has one live instance at any point in time. In effect we have a pre-condition:

$$
\begin{aligned}
& \text { pre_fstep }_{T}(\rho) \kappa \quad \text { where } \quad T(\kappa)=\left(\operatorname{PAR},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \\
& \widehat{=} \quad k \in \operatorname{dom} \rho \Rightarrow \rho(k)=\operatorname{VAL} 0 \\
& \quad \text { where } k=\left(\kappa, \_\mathrm{t}\right)
\end{aligned}
$$

The next execution point of a child of a parallel statement depends on whether or not any other threads remain executing. The parallel construct's thread count is decremented. If there are still outstanding threads we simply discard the execution point, and return the modified environment. If that was the last thread, then we seek the next statement after the parallel construct:

$$
\begin{aligned}
& \operatorname{nxtep}_{T}(\rho)(i: \kappa) \text { where } T(\kappa)=\left(\operatorname{PAR},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \\
& \widehat{=} \quad \text { let } \rho^{\prime}=\rho \ominus\{k \mapsto 1\} \text { in } \\
& \rho^{\prime}(k)>0 \rightarrow\left(\emptyset, \rho^{\prime}\right) \\
& \rho^{\prime}(k)=0 \rightarrow \operatorname{nxtep}_{T}\left(\rho^{\prime}\right) \kappa \\
& \text { where } k=\left(\kappa, \_\mathrm{t}\right)
\end{aligned}
$$

When there are other threads remaining we go from

to


When there are no other threads remaining we go from

to


### 4.5.5 The Brk Statement

The Break statement simply jumps to the first statement after the most immediately enclosing while-loop, or pri-alt construct. In our reduced abstract syntax, we don't have explicit breaks in pri-alt, so we view them here as only occuring inside while-loops. While technically an atomic statement, it is treated
here as composite one. It is called by fstep, but not by nxtep:

$$
\begin{aligned}
& \text { fstep }_{T}(\rho) \kappa \quad \text { where } T(\kappa)=(\operatorname{BRK}, \Lambda) \\
& \quad \widehat{=} \operatorname{nxtep}_{T}(\rho)\left(\operatorname{brk}_{T} \kappa\right)
\end{aligned}
$$

where
brk : $\quad T C \rightarrow T C$
$\operatorname{brk} \Lambda \widehat{=} \Lambda$
$\operatorname{brk}(i: \kappa) \widehat{=} T(i: \kappa)=\left(\mathrm{WhL}_{\mathrm{H}} \ldots\right) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa$
$\rightarrow$ brk $\kappa$
Pictorially, we go from

$$
\begin{gathered}
\kappa:: \text { WHL } \\
\\
\vdots \\
\text { fstep } \Rightarrow j:-:(i: \kappa):: \text { BRK }
\end{gathered} \quad \text { (no WHL here) }
$$

to
nxtep $\Rightarrow \kappa$ :: WHL

$\vdots \quad$ (no Whl here)
$j:-:(i: \kappa)::$ BRK

### 4.5.6 The Pri Statement

The effect of fstep on the prialt has already been covered. The next statement for a prialt sub-statement is simply the next statement for the prialt itself:

$$
\begin{aligned}
& \operatorname{nxtep}_{T}(\rho)(i: \kappa) \quad \text { where } \quad T(\kappa)=\left(\operatorname{PRI},\left\langle c_{1}, \ldots, c_{N}\right\rangle\right) \\
& \quad \widehat{=} \operatorname{nxtep}_{T}(\rho) \kappa
\end{aligned}
$$

Pictorially, we go from

to


### 4.6 Definition of resep

The resep function, handles the prialt and communication constructs. Unlike, the other functions, it is given a set of execution pointers denoting all the currently "live" prialts and input and output statements.

$$
\begin{aligned}
& \text { resep } \quad: \quad \text { Stmt } T \rightarrow E n v \rightarrow \mathcal{P} T C \rightarrow \text { PState } \\
& \text { pre-resep }{ }_{T}(\rho) K \widehat{=} \quad \forall\left[\mathrm{isComm}_{T}\right] K \\
& \text { isComm : Stmt } \rightarrow T C \rightarrow \mathbb{B} \\
& \operatorname{isComm}_{T}(\kappa) \hat{=} \text { case } T(\kappa) \\
& \left(\mathrm{IN}_{-}, \Lambda\right) \rightarrow \text { True } \\
& \text { (Out } \_ \text {, } \Lambda \text { ) } \rightarrow \text { True } \\
& \text { (PRI }-, \text { ) } \rightarrow \text { TRUE } \\
& s \rightarrow \text { FALSE }
\end{aligned}
$$

It determines which cases in these will become active, if any. A prialt without a default clause can itself be blocked, in the same way as is possible for the communications statements. If a prialt is blocked we return a pointer to the whole construct. When a prialt alternative is selected, the relevant communications statement is executed. The associated statements are executed in subsequent clock cycles.

### 4.6.1 Definition of resep, without prialts

We now give a definition of resep that does not cater for Pri. This means in effect that it changes nothing, as the only statments we expect to handle are atomic communication statements which are already handled.

$$
\begin{aligned}
\text { resep } & : S t m t T \rightarrow E n v \rightarrow \mathcal{P} T C \rightarrow \text { PState } \\
\operatorname{resep}_{T}(\rho) K & \widehat{=}(K, \rho)
\end{aligned}
$$

### 4.7 Full Definitions of fstep and nxtep

The definitions of fstep and nxtep are distributed throughout the preceding text. Here we gather them together in one place.

### 4.7.1 Complete Definition of fstep

```
        pre_fstep \({ }_{T}(\rho) \kappa\)
        \(\widehat{=} \kappa \in \operatorname{dom} T\)
            \(\wedge\)
            case \(T(\kappa)\)
                    \(\left(\mathrm{PAR},{ }_{-}\right) \rightarrow k \in \operatorname{dom} \rho \wedge \rho(k)=\operatorname{VAL} 0\)
                where \(k=\left(\kappa, \_\mathrm{t}\right)\)
            otherwise \(\rightarrow\) TRUE
        fstep \(_{T}(\rho) \kappa\)
        \(\widehat{=}\) case \(T(\kappa)\)
            \(\left(\mathrm{SEQ}, \_\right) \rightarrow \mathrm{fstep}_{T}(\rho)(1: \kappa)\)
            \((\operatorname{PAR}, \sigma) \rightarrow \operatorname{fstpar}_{T}\left(\emptyset, \rho \dagger\left\{\left(\kappa, \_\mathrm{t}\right) \mapsto \operatorname{VAL} n\right\}\right) \kappa n\)
                        where \(n=\) len \(\sigma\)
            \(\left(\right.\) ASG \(\left._{-}, \Lambda\right) \rightarrow(\{\kappa\}, \rho)\)
            \(\left(\mathrm{IN}_{-}, \Lambda\right) \rightarrow(\{\kappa\}, \rho)\)
            (OUT \(\left.{ }_{-}, \Lambda\right) \rightarrow(\{\kappa\}, \rho)\)
            \(\left(\operatorname{IF} c,\left\langle s_{t}, s_{e}\right\rangle\right) \mid \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa)\)
            \(\left(\operatorname{IF} c,\left\langle s_{t}, s_{e}\right\rangle\right) \mid \neg \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(2: \kappa)\)
            \(\left(\operatorname{IF} c,\left\langle s_{t}\right\rangle\right) \mid \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa)\)
            \(\left(\operatorname{IF} c,\left\langle s_{t}\right\rangle\right) \mid \neg \rho(c) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
            \(\left(\right.\) WHL \(\left.c,{ }_{-}\right) \mid \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa)\)
            (WHL \(c,{ }_{-}\)) \(\mid \neg \rho(c) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
            \((\) PRI, \()) \rightarrow\left(\{\kappa\}, \rho \dagger\left\{\left(\kappa, \_s\right) \mapsto\right.\right.\) STOP \(\left.\}\right)\)
            \((\operatorname{BRK}, \Lambda) \rightarrow \operatorname{nxtep}_{T}(\rho)\left(\mathrm{brk}_{T} \kappa\right)\)
            \((\operatorname{DLY}, \Lambda) \rightarrow(\{\kappa\}, \rho)\)
            fstpar \(: \quad\) StmtT \(\rightarrow\) PState \(\rightarrow T C \rightarrow \mathbb{N} \rightarrow \mathcal{P} T C \times\) Env
    \(\operatorname{fstpar}_{T}(P, \rho) \kappa 0 \widehat{=}(P, \rho)\)
\(\operatorname{fstpar}_{T}(P, \rho) \kappa(i+1) \widehat{=} \operatorname{let}\left(P^{\prime}, \rho^{\prime}\right)=\operatorname{fstep}_{T}(\rho)(i+1: \kappa)\)
                        in \(\operatorname{fstpar}_{T}\left(P \cup P^{\prime}, \rho^{\prime}\right) \kappa i\)
            brk : \(\quad T C \rightarrow T C\)
            \(\operatorname{brk} \Lambda \hat{=} \Lambda\)
            \(\operatorname{brk}(i: \kappa) \widehat{=} T(i: \kappa)=(\mathrm{WHL} \ldots) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
                \(\rightarrow\) brk \(\kappa\)
```


### 4.7.2 Complete Definition of nxtep

```
\(\operatorname{nxtep}_{T}(\rho)(i: \kappa)\)
    \(\widehat{=} \quad\) case \(T(i: \kappa)\)
                \((\operatorname{In} c v, \Lambda) \mid \operatorname{stateOf}(\rho(i: \kappa, c))=\operatorname{InPUT} \rightarrow(\{i: \kappa\}, \rho)\)
                (Out \(c e, \Lambda) \mid \operatorname{stateOf}(\rho(i: \kappa, c))=\) Output \(\rightarrow(\{i: \kappa\}, \rho)\)
                \((\mathrm{PRI}, \sigma) \mid \rho\left(i: \kappa, \_\right.\)s \()=\operatorname{STOP} \rightarrow(\{i: \kappa\}, \rho)\)
            otherwise
                case \(T(\kappa)\)
                    \((\mathrm{SEQ}, \sigma) \mid i<\operatorname{len} \sigma \rightarrow \operatorname{fstep}_{T}(\rho)(i+1: \kappa)\)
            \((\mathrm{SEQ}, \sigma) \mid i=\operatorname{len} \sigma \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
            \(\left(\mathrm{IF}_{-}, \sigma\right) \rightarrow \mathrm{nxtep}_{T}(\rho) \kappa\)
            \(\left(\right.\) WHL \(\left.c,{ }_{-}\right) \mid \rho(c) \rightarrow \operatorname{fstep}_{T}(\rho)(1: \kappa)\)
            \(\left(\right.\) WhL \(\left.c,{ }_{-}\right) \mid \neg \rho(c) \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
            \(\left(\operatorname{PAR},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \mid \rho^{\prime}(k)>0 \rightarrow\left(\emptyset, \rho^{\prime}\right)\)
                where \(k=\left(\kappa, \_\mathrm{t}\right)\) and \(\rho^{\prime}=\rho \ominus\{k \mapsto 1\}\)
            \(\left(\operatorname{PAR},\left\langle s_{1}, \ldots, s_{N}\right\rangle\right) \mid \rho^{\prime}(k)=0 \rightarrow \operatorname{nxtep}_{T}\left(\rho^{\prime}\right) \kappa\)
                where \(k=\left(\kappa, \_\mathrm{t}\right)\) and \(\rho^{\prime}=\rho \ominus\{k \mapsto 1\}\)
            \((\text { PRI, })^{\text {) }} \rightarrow \operatorname{nxtep}_{T}(\rho) \kappa\)
```


### 4.8 Auxilliary Functions

We set channel state to waiting for output when we have a input statement using an idle channel:

$$
\begin{aligned}
\text { setOutWait } & : T C \rightarrow \text { ValState } \rightarrow \text { ValState } \\
\text { setOutWait }\left[\kappa_{i}\right](\text { Chan Idle } \Lambda \Lambda d) & \widehat{=}\left(\text { Chan OuTWait } \kappa_{i} \Lambda d\right) \\
\left.\operatorname{setOutWait}\left[\kappa_{i}\right] \text { (OuTCh IdLE } \Lambda d\right) & \widehat{=}\left(\text { OuTCh OuTWAit } \kappa_{i} d\right)
\end{aligned}
$$

We set channel state to waiting for input when we have a output statement using an idle channel:

$$
\begin{array}{rc}
\text { setInWait } & : T C \rightarrow \text { ValState } \rightarrow \text { ValState } \\
\operatorname{setInWait}\left[\kappa_{o}\right](\text { Chan Idle } \Lambda \Lambda d) & \widehat{=}\left(\text { Chan InWAit } \Lambda \kappa_{0} d\right) \\
\operatorname{setInWait}\left[\kappa_{o}\right](\text { OUTCH IdLE } \Lambda d) & \widehat{=}\left(\text { OUTCH InWAIT } \kappa_{o} d\right)
\end{array}
$$

We set channel state to ready when we have an input statement using a channel waiting for input:

$$
\text { setInReady } \quad: \quad T C \rightarrow \text { ValState } \rightarrow \text { ValState }
$$

$$
\operatorname{set} \operatorname{In} R e a d y\left[\kappa_{i}\right]\left(\text { Chan InWait } \Lambda \kappa_{o} d\right) \widehat{=} \quad\left(\text { Chan Ready } \kappa_{i} \kappa_{o} d\right)
$$

We also set channel state to ready when we have an output statement using a channel waiting for output:

$$
\begin{aligned}
\text { setOutReady } & : T C \rightarrow \text { ValState } \rightarrow \text { ValState } \\
\text { setOutReady }\left[\kappa_{o}\right]\left(\text { Chan OuTWAIT } \kappa_{i} \Lambda d\right) & \widehat{=}\left(\text { CHAN READY } \kappa_{i} \kappa_{o} d\right)
\end{aligned}
$$

We also set channel state to ready when we have both input and output statements using a channel waiting for output:

$$
\begin{aligned}
\text { setReady } & :(T C)^{2} \rightarrow \text { ValState } \rightarrow \text { ValState } \\
\text { setReady }\left[\kappa_{i}, \kappa_{o}\right](\text { Chan OutWait } \Lambda \Lambda d) & \widehat{=}\left(\text { Chan READY } \kappa_{i} \kappa_{o} d\right)
\end{aligned}
$$

## 5 Acknowledgments

We would like to thank Jim Woodcock and Alistair McEwan for their many helpful discussions and comments, as well as Ian Page for his help in identifying the key parts of the language. We would also like to thank the Dean of Research of Trinity College Dublin for his support.

## References

[BHP94] J. P. Bowen, He Jifeng, and I. Page. Hardware compilation. In J. P. Bowen, editor, Towards Verified Systems, volume 2 of Real-Time Safety Critical Systems, chapter 10, pages 193-207. Elsevier, 1994.
[Emb] Embedded Solutions Ltd. (now Celoxica Ltd.). Handel-C Language Reference Manual, v2.1.
[Hoa90] C.A.R. Hoare. Communicating Sequential Processes. Intl. Series in Computer Science. Prentice Hall, 1990.
[LKL+95] Adrian Lawrence, Andrew Kay, Wayne Luk, Toshio Nomura, and Ian Page. Using reconfigurable hardware to speed up product development and performance. In Will Moore and Wayne Luk, editors, Field-Programmable Logic and Applications, pages 111-119. Springer-Verlag, Berlin, August/September 1995. Proceedings of the 5th International Workshop on Field-Programmable Logic and Applications, FPL 1995. Lecture Notes in Computer Science 975.
[PL91] I. Page and W. Luk. Compiling Occam into field-programmable gate arrays. In W. Moore and W. Luk, editors, FPGAs, Oxford Workshop on Field Programmable Logic and Applications, pages 271-283. Abingdon EE\&CS Books, 15 Harcourt Way, Abingdon OX14 1NV, UK, 1991.
[SP93] M. Spivey and I. Page. How to design hardware with Handel. Technical report, Oxford University Hardware Compilation Group, December 1993.

